

For:



Transmitted herewith for filing is the Patent Application of:

Inventor: HUNG-WEI CHEN, HSUN-CHIN TSAO, KUANG-HSIN CHEN, DI-HOUNG LEE

METHOD FOR REDUCING A SHORT CHANNEL EFFECT FOR NMOS DEVICES IN SOI CIRCUITS

Enclosed are:	
Eliciosed are.	
X 6 sheets of drawing(s) - formal.	
X An assignment of the invention to Taiwan Semiconductor Manufacturing Co.	
An associate power of attorney Applicant claims small entity status	
Request & Certification under 35 USC 122(b)(2)(b)(i)	
The filing fee has been calculated as shown below:	
(Col. 1) (Col. 2) OTHER THAN A SMALL ENTITY	
FOR: NO. FILED NO. EXTRA RATE FEE	
BASIC FEE \$ 770.	
TOTAL CLAIMS 29 -20= 9 x 18 = \$ 162.	
INDEP CLAIMS 2 - 3 = 0 x 86 = \$ 0.	
SUB TOTAL \$ 932.	
ASSIGNMENT \$40.	
TOTAL \$ 972.	
X Please charge my Deposit Account No. 19-0033 in the amount of \$ 972. A duplicate copy of this sheet is enclosed.	
The Commissioner is hereby authorized to charge payment of the following fees associated with this communication	an anadit anu
overpayment to Deposit Account No. 19-0033. A duplicate copy of this sheet is enclosed.	or credit any
X Any additional filing fees required under 37 CFR §1.16.	
X Any patent application processing fees under 37 CFR §1.17.	
Respectfully submitted,	

EXPRESS MAIL CERTIFICATE

Express Mail No.EV319114232US

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA, 22313-1450. Applicant and Attorney requests the date of deposit as the Filing Date.

3 3 04 Date of deposit

Signature / Date